L Number	Hits	Search Text	DB	Time stamp
-	151	(bus with switch$3) same control$4 same interfac$3 same clock same select$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/19 10:49
-	1	(dual adj3 clock$3) same (bus adj3 switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:37
-	444	data with bus with switch with clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:38
-	414	dual adj clock$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:42
-	2252	bus adj switch$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:42
-	0	(dual adj clock$3) same (bus adj switch$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:42
-	1	(dual adj clock$3) and (bus adj switch$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:42
-	4	bus adj (speed same switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 15:43
-	3543	microprocessor same interface same control same clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 15:48
-	16	(microprocessor same interface same control same clock$1) and (bus adj switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 15:52
-	927	dual adj3 clock$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 15:52
-	11	(dual adj3 clock$3) same switch same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 16:05
-	9	kume.in. and nagao.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 16:07

-	643	microprocessor with (semiconductor adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 10:09
-	15	(microprocessor with (semiconductor adj chip)) same advantage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 10:06
-	29	microprocessorsame built same (semiconductor adj chip) same advantage$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 10:09
-	0	microprocessor same built same (semiconductor adj chip) same advantage$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 10:09
-	0	micro-processor same built same (semiconductor adj chip) same advantage$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 10:46
-	2690	frequency adj division adj ratio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 12:45
-	1051	(frequency adj division adj ratio) same clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 12:46
-	71	(frequency adj division adj ratio) same clock same output$4 same (multiple plural plurality)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 12:46
-	2211	clock adj control adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 13:14
-	18	(clock adj control adj circuit) same multiple same ratio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 13:14
-	6008	BSC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 10:14
-	286	BSC same clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 10:15
-	15794	((multi multiple plural plurality) adj3 clock$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 11:21

-	3680	((multi multiple plural plurality) adj3 clock$1) same select$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 11:22
-	100	((multi multiple plural plurality) adj3 clock$1) same select$3 same ratio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 11:22
-	236112	clock$1 same output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 12:54
-	14941	clocks with output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 12:55
-	209	clocks with output with multiple	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 12:55
-	1	VL82C315A	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 13:24
-	98	SCAMP	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 13:24
-	39	((slow same fast) adj3 device$1) same clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 16:38
-	3042	((slow same fast)) same clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 16:38
-	445	((slow same fast)) same clocks	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 16:38
-	0	(clock with (selection adj switch)) same (fast with slow)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 17:43
-	12	(clock with (select$3 adj switch)) same (fast with slow)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/15 17:43
-	26	(CLOCK ADJ OUTPUT) with (selec$3 adj switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 10:41

-	229	different with speed with device$1 with clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 10:42
-	23013	(data adj processor).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 14:38
-	334	(clock$1 same (data adj processor)).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 14:38
-	0	(clock2 same (data adj processor)).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 14:38
-	14	(clocks same (data adj processor)).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 14:41
-	38	(two with different with clock$1) same (fast with slow)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/16 16:45
-	328	two with separate with clocks	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 12:32
-	21	(fast with slow) same device same clocks same output$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 12:42
-	1149	generat$3 same two same clocks same (different sepatrate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 13:09
-	171	generat$3 same (two adj clocks) same (different sepatrate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 13:09
-	2238	different with speed with devices	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 14:12
-	9	different adj speed adj devices	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 14:09
-	12548	(different adj speed) clocks with devices	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 14:13

-	0	(different adj speed) with clocks with devices	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 14:15
-	0	suspend$3 same execut$3 same instruction$1 same switch$3 same clock$1 same acknowledg$5 same synchroniz$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 15:46
-	2	suspend$3 same execut$3 same instruction$1 same switch$3 same clock$1 same synchroniz$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 15:47
-	24	suspend$3 same execut$3 same instruction$1 same clock$1 same synchroniz$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/18 15:47
-	74960	((high fast) same (low slow)) adj3 (device speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 15:16
-	54112	((high fast) same (low slow)) adj (device speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 15:16
-	41845	((high fast) with (low slow)) adj (device speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 15:17
-	106	((high fast) with (low slow)) adj speed adj device$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 15:17
-	117	pcb same clock same device$1 same board	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:07
-	1127	((daughter same mother) adj board)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:14
-	44	((daughter same mother) adj board) same (pc (personal adj computer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:10
-	1	((daughter same mother) adj board) same (clock$1 adj3 wire$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:22
-	24	((daughter same mother) adj board) same clock$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:16

-	0	((daughter same mother) adj board) same ((cpu same bus) adj clock$1 adj3 wire$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:22
-	0	((daughter same mother) adj board) same ((cpu same bus) adj clock$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:22
-	5	(pcb board) same ((cpu same bus) adj clock$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 16:22